SPEED VIOLATION DETECTION SYSTEM(SVDS)



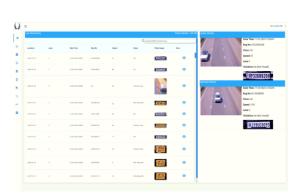
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At a Glance

Speed Violation Detection System (SVDS) consists of a fixed ANPR camera, a fixed Radar and an attached Video Image processing unit. SVDS shall perform speed violation detection at designated spots on the highway section and optionally provide local warning to travellers using Speed Displays. The System shall communicate such detections to the ATMS Control Centre for storage, analysis, eChallaning & reporting. Sub-system Components are as follows:

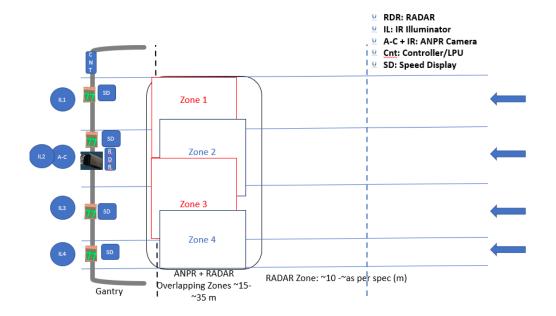
- RADAR.
- ANPR Camera

- Speed Display
- Aggregator PC (at Control Room)





Subsystem Component layout





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Specifications of Roadside Cabinet (LPU)

Parameters	Specifications
Operating Voltage	220 V, 1 phase, 50 Hz AC
Power consumption	20 Watt
Protection	Short Circuit, Overload, Overvoltage, ESD
Operating Temperature	-20°C to +65°C
IP Protection Rating	IP 65
CPU-GPU	6/8-core ≥2 GHz
Memory	SSD 512 GB – as per Project requirements
OS	Linux
Connectivity	10/100/1000 BASE-T ETHERNET, RS-485
Cabinet Size	450 mm (L) x 450 mm (H) x 220 mm (D) (Approx.)
Material	MS (Powder Coated)
Mounting Arrangement	Gantry Mounted / Stand Mounted

SVDS Reporting module

